

Silicon PNP Power Transistors

2SA1294

DESCRIPTION

- With TO-3PN package
- Complement to type 2SC3263

APPLICATIONS

- Audio and general purpose

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

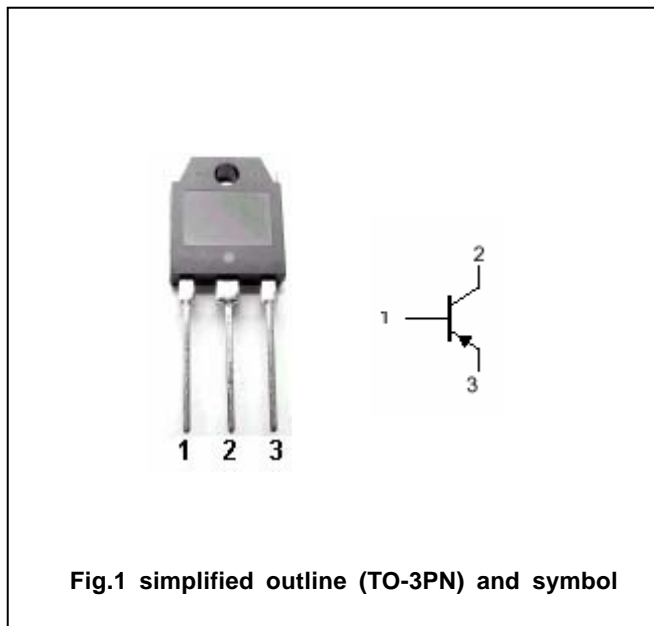


Fig.1 simplified outline (TO-3PN) and symbol

Absolute maximum ratings(Ta=)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-230	V
V_{CEO}	Collector-emitter voltage	Open base	-230	V
V_{EBO}	Emitter-base voltage	Open collector	-5	V
I_C	Collector current		-15	A
I_B	Base current		-4	A
P_C	Collector power dissipation	$T_C=25$	130	W
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

Silicon PNP Power Transistors

2SA1294

CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO}	Collector-emitter breakdown voltage	I _C =-25mA ; I _B =0	-230			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-5A; I _B =-0.5A			-2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =-230V; I _E =0			-100	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-100	μ A
h _{FE}	DC current gain	I _C =-5A ; V _{CE} =4V	50		140	
C _{ob}	Output capacitance	I _E =0 ; V _{CB} =-10V; f=1MHz		500		pF
f _T	Transition frequency	I _E =2A ; V _{CE} =-12V		35		MHz

Switching times

t _{on}	Turn-on time	I _C =-5A; R _L =12 I _{B1} =-I _{B2} =-0.5A V _{CC} =-60V		0.35		μ s
t _s	Storage time			1.5		μ s
t _f	Fall time			0.3		μ s

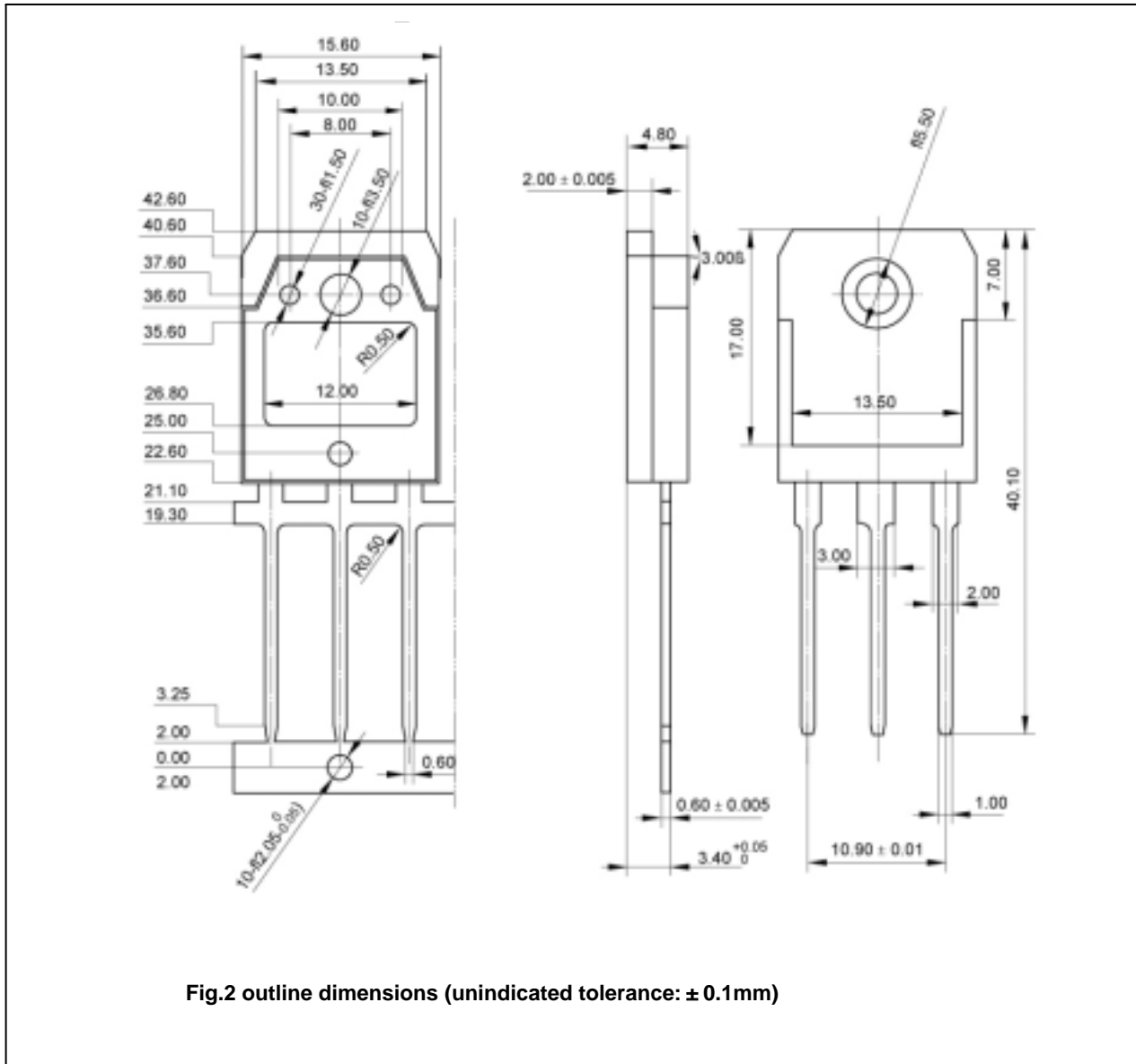
◆ h_{FE} Classifications

O	Y
50-100	70-140

Silicon PNP Power Transistors

2SA1294

PACKAGE OUTLINE



Silicon PNP Power Transistors

2SA1294

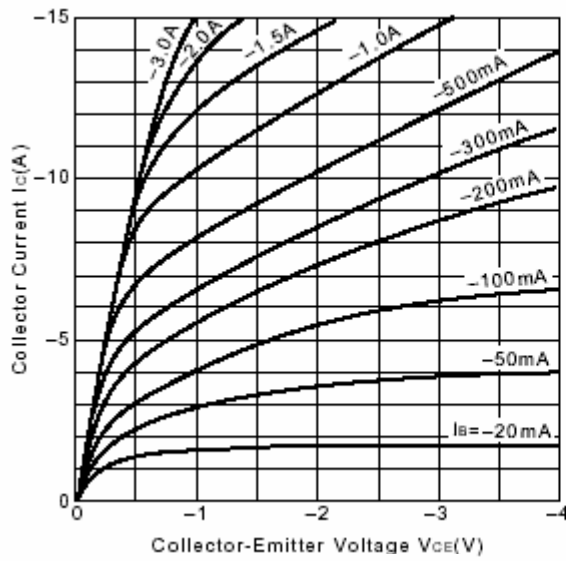


Fig.3 Static Characteristic

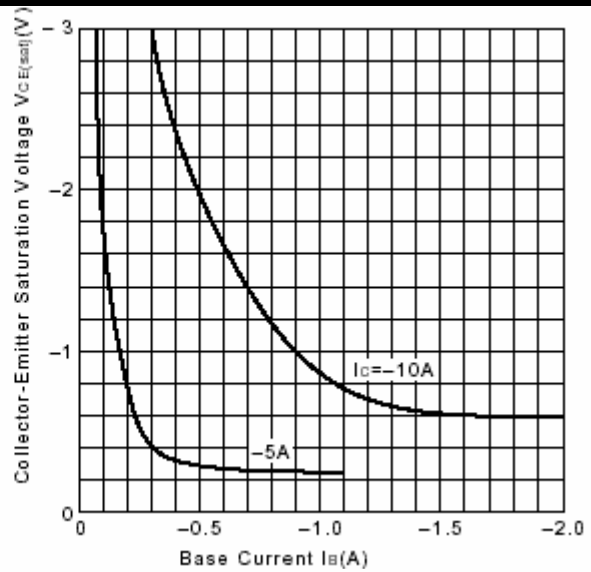


Fig.4 $V_{ce(sat)}-I_b$ Characteristics

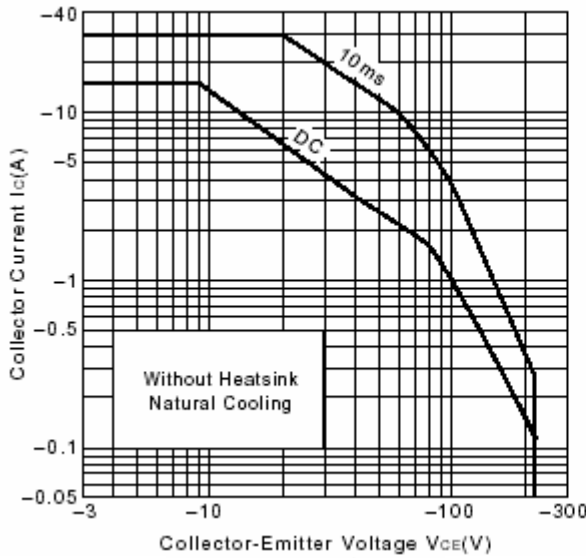


Fig.5 Safe Operating Area

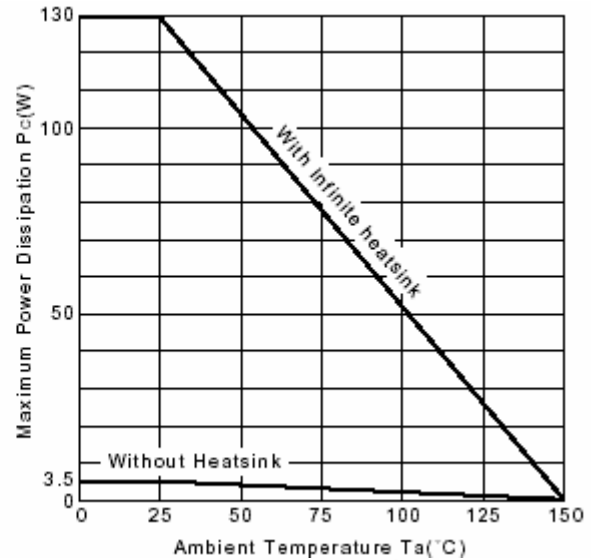


Fig.6 P_c-T_a Derating

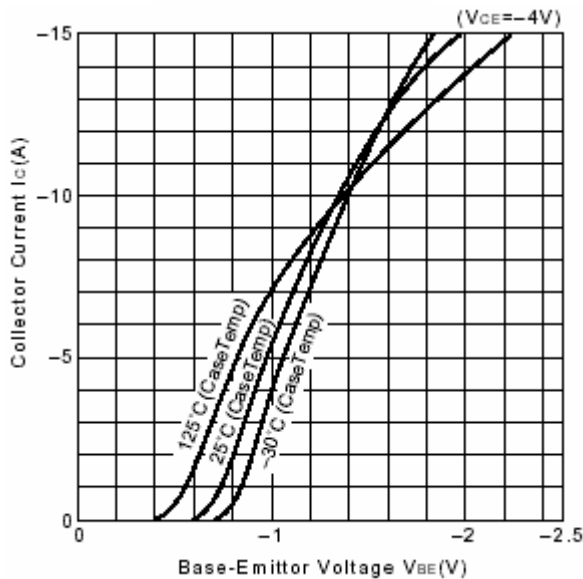


Fig.7 I_c-V_{BE}

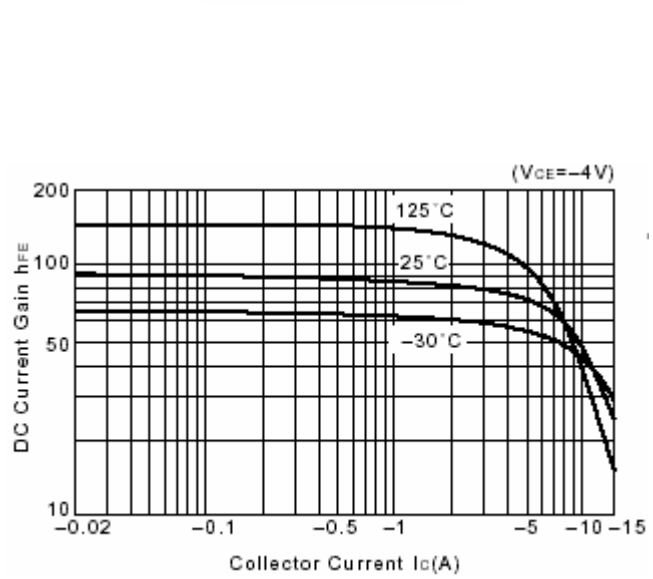


Fig.8 DC current Gain